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Code:

Project: Hybris

Area: Energy Efficiency in Systems, Buildings and Communities Area

Area leader:

Group: Power Systems

Group leader: Dr. Jose Luis Domínguez García

TITLE: Junior Engineer/Early Stage Researcher in the field of Power Electronics

JOB DESCRIPTION

The Power Systems Group announces the opening of a position as Junior Engineer / Early Stage Researcher to work on the HYBRIS H2020 project in the field of Power Electronics. The tasks to be carried out will be focused to design, model, experimental development of innovative Power Converters.

Qualifications and experience required:

Essential:

- Bachelor or Master in Industrial Electronics and Automatic Control, Electronics, Telecommunications engineering or similar.
- Design or operation of power converters
- Experience in electronics modeling and simulation software
- Hands-on experience with power electronics is a must

Preferred:

- Experience in programming.
- Experience with communications and automation systems.
- Experience with control boards design and programming
- Knowledge regarding industrial electronic systems
- Knowledge on Matlab and Altium.
- Knowledge on PCB design

Language required:

Fluent in English. (knowledge of Spanish and/or Catalan is a plus)

Personal Skills:

- Team Worker
- Initiative in Research and Innovation
- Flexibility
- Results-oriented
- Analytical and synthesis capabilities

What we offer:

Salaries will be paid in accordance with the IREC's salary policy, depending on the candidate's qualification and professional experience.

How to apply:

Send applications by email directly to HR office (irecjobs@irec.cat) including CV, academic and professional records, and motivation letter.

Please, be aware that the evaluation will be done in a continuous manner, and the position will close as soon as a candidate is selected.