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Group: Power Systems Group

Group leader: José Luis Domínguez

JOB DESCRIPTION

The Power Systems Group announces the opening of a position as Junior Researcher to work on a research project to develop a new technology for enhance functionalities of existing RTUs and SmartMeters of the electrical grid. The tasks to be carried out will be focused to development and programming of tools into control boards for testing and validation in lab conditions. This position is funded by SDN-enabler project funded by Generalitat de Catalunya (AGAUR) under the Llabor programme. Additionally, experimental activities and set ups within IREC's lab facilities are expected.

Qualifications and experience required:

Essential:

- Bachelor or Master in Electrical Engineering, Industrial Electronics and Automatic Control, Telecommunications engineering, Computer Science or similar.
- Experience with control boards design and programming
- Experience in programming e.g. Python, C++,....
- Experience with communications and automation systems.

Other expertise (not mandatory):

- Experience in participation in collaborative projects.
- Knowledge on GIT
- Experience in smart meters and other hardware devices on smart grids

Language required:

Fluent in English. (knowledge of Spanish and/or Catalan is a plus)

Personal Skills:

- Team Worker
- Initiative in Research and Innovation
- Flexibility
- Results-oriented
- Analytical and synthesis capabilities

What we offer:

We offer project related position (9 months) with potential extension. Salaries will be paid according to IREC's internal regulation and candidate.

How to apply:

Send applications by email directly to HR office (irecjobs@irec.cat) and Jose Luis Domínguez (jldominguez@irec.cat) including CV, academic and professional records, and motivation letter.

Please, be aware that the evaluation will be done in a continuous manner, and the position will close as soon as a candidate is selected.

Deadline for applications: 15 November 2022