

Publish date April 18th 2023

Ref. 31/2023

Code:

Project: IPLUG

Area: Energy Efficiency in Systems, Buildings and Communities Area

Area leader:

Group: Power Systems

Group leader: Dr. Jose Luis Domínguez García

TITLE: Researcher on future distribution networks with high penetration of power converters

JOB DESCRIPTION

The Power Systems Group announces a new opening as Research Engineer to work on IPLUG project research project in the field of future distribution networks. These positions are related to the development of the tools for control, monitoring, operation and resilience of electrical networks power converters. Such analysis will include design and validation of tools for the electrical networks as lab testing or field application.

Qualifications and experience required:

Essential:

- Bachelor, Master or PhD in electrical engineering or related.
- Knowledge in network operation
- Knowledge in grid studies
- Good use of Python
- Knowledge of Matlab Simulink, DigSilent Power Factory, etc

Preferred:

- Knowledge in converter control and coordination
- Knowledge of Machine Learning
- Knowledge in HIL systems
- Experience in working in/with network operators
- Experience in work in international environment.
- Experience in work in collaborative projects.

Language required:

Fluent in English. Spanish desirable

Personal Skills:

- Team Worker
- Initiative in Research and Innovation
- Flexibility
- Results-oriented

- Analytical and synthesis capabilities

What we offer:

We offer fix-term position, with potential extension. Salaries will be paid in accordance with the IREC's salary policy, depending on the candidate's qualification and professional experience.

Starting Date:

The expected starting Date will be the sooner the better, but the candidate is expected to start in **Summer 2023 the latest** (However, other dates could be agreed)

How to apply:

Send applications by email directly to Francesc Torregrosa (irecjobs@irec.cat) including CV, academic and professional records and motivation letter